

REMARKS

Claims 1-34 are now pending in the application. The Examiner is respectfully requested to reconsider and withdraw the rejections in view of the amendments and remarks contained herein.

REJECTION UNDER 35 U.S.C. § 102

Claims 1-31 stand rejected under 35 U.S.C. § 102(b) as being anticipated by Sugimoto (U.S. Pat. No. 5,777,610). This rejection is respectfully traversed.

Applicant thanks the examiner for the courtesy extended to its representative during the interview on September 10, 2003. During the interview, the grounds for rejection were discussed as well as possible amendments to the claims to distinguish the invention over the prior art. The amendments contained herein reflect the discussion with the examiner.

Amended claim 1 calls for a driver integrated circuit mounted on an extended area of a substrate and a circuit board provided above the driver integrated circuit. The circuit board includes one of a signal-output terminal portion and a scanning-output terminal portion positioned within the extended area. The other of the signal-output terminal portion and the scanning-output terminal portion extends outside the extended area.

This configuration is most clearly illustrated in Fig. 2 and described at pages 19 and 20 of applicant's specification. As shown in Fig. 2, the circuit board 22 includes a signal-output terminal portion 22A and a scanning-output terminal portion 22B. The signal-output terminal portion 22A is positioned within the extended area 23A. The

scanning-output terminal portion 22B extends outside the extended area 23A.

In contrast, Sugimoto teaches wiring boards 19 and integrated circuits 16. Sugimoto also teaches wiring boards 13 and integrated circuits 15. As shown in Fig. 6, each wiring board 19 includes output terminals 302. Each wiring board 13 includes output terminals 104. However, neither wiring board 19 nor wiring board 13 includes a signal-output terminal portion and a scanning-output terminal portion as claimed. Further, neither wiring board 19 nor wiring board 13 includes one of two output terminal portions within an extended area of the substrate.

Claim 2 also calls for a control circuit board including a signal-output terminal portion and a scanning-output terminal portion. One of the signal-output terminal portion and the scanning-output terminal portion is contained within an extended area of one of the substrates. The other of the signal-output terminal portion and the scanning-output terminal portion extends outside of the extended area.

As stated above with respect to claim 1, Sugimoto fails to teach a control circuit board having a signal-output terminal portion and a scanning-output terminal portion. Sugimoto also fails to teach that one of a signal-output terminal portion and a scanning-output terminal portion is contained within an extended area while the other of the signal-output terminal portion and the scanning-output terminal portion extends outside of the extended area.

Claim 3 calls for a control circuit board including a signal-output terminal portion and a scanning-output terminal portion. One of the signal-output terminal portion and the scanning-output terminal portion is contained within a plane region of a first extended area or a second extended area. The other of the signal-output terminal

portion and the scanning-output terminal portion extends outside of the first or second extended area to the other extended area.

As shown in Fig. 2 of applicant's specification, the signal-output terminal portion 22A is positioned within the extended area 23A. The scanning-output terminal portion 22B extends outside the extended area 23A to the other extended area 24A. Sugimoto fails to teach a wiring board with a signal-output terminal portion and a scanning-output terminal portion. Sugimoto also fails to teach one of a signal-output terminal portion and a scanning-output terminal portion positioned within a first extended area and the other of a signal-output terminal portion and a scanning-output terminal portion extending outside the extended area to a second extended area.

Claims 4-10 are dependent claims and should be in condition for allowance for at the same reasons as set forth with respect to their base claims.

Claim 11 calls for a circuit board including one of a signal-output terminal portion and a scanning-output terminal portion contained within an extended area while the other of the signal-output terminal portion and the scanning-output terminal portion extends outside the extended area. As set forth above, Sugimoto fails to teach such an arrangement.

Claim 12 calls for a circuit board provided at least above a scanning driver integrated circuit mounted in a first extended area or a data-signal driver integrated circuit mounted in a second extended area. The circuit board includes one of a signal-output terminal portion and a scanning-output terminal portion positioned within a plane region of one of the first and second extended areas while the other of the signal-output terminal portion and the scanning-output terminal portion extends outside the other of

the first and second extended areas. An input terminal portion of the scanning driver integrated circuit mounted in the first extended area is connected to the scanning-output terminal portion. An input terminal portion of the data-signal driver integrated circuit mounted in the second extended area is connected to the signal-output terminal portion of the circuit board.

As stated above, Sugimoto fails to teach such an arrangement.

Claims 13-31 are dependent claims and should be in condition for allowance for at least the same reasons as set forth with respect to their base claims.

NEW CLAIMS

New claims 32 - 34 are presented herein. Claims 32 and 33 further define the subject matter of independent claims 1 and 2. Claim 34 varies the scope of protection for the applicant. Support for these amendments and new claims can be found throughout the application as originally filed. No new matter is added. Favorable consideration of these new claims is respectfully requested.

CONCLUSION

It is believed that all of the stated grounds of rejection have been properly traversed, accommodated, or rendered moot. Applicant therefore respectfully requests that the Examiner reconsider and withdraw all presently outstanding rejections. It is believed that a full and complete response has been made to the outstanding Office Action, and as such, the present application is in condition for allowance. Thus, prompt and favorable consideration of this amendment is respectfully requested. If the

Examiner believes that personal communication will expedite prosecution of this application, the Examiner is invited to telephone the undersigned at (248) 641-1600.

Respectfully submitted,

Dated: Oct. 2, 2003

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